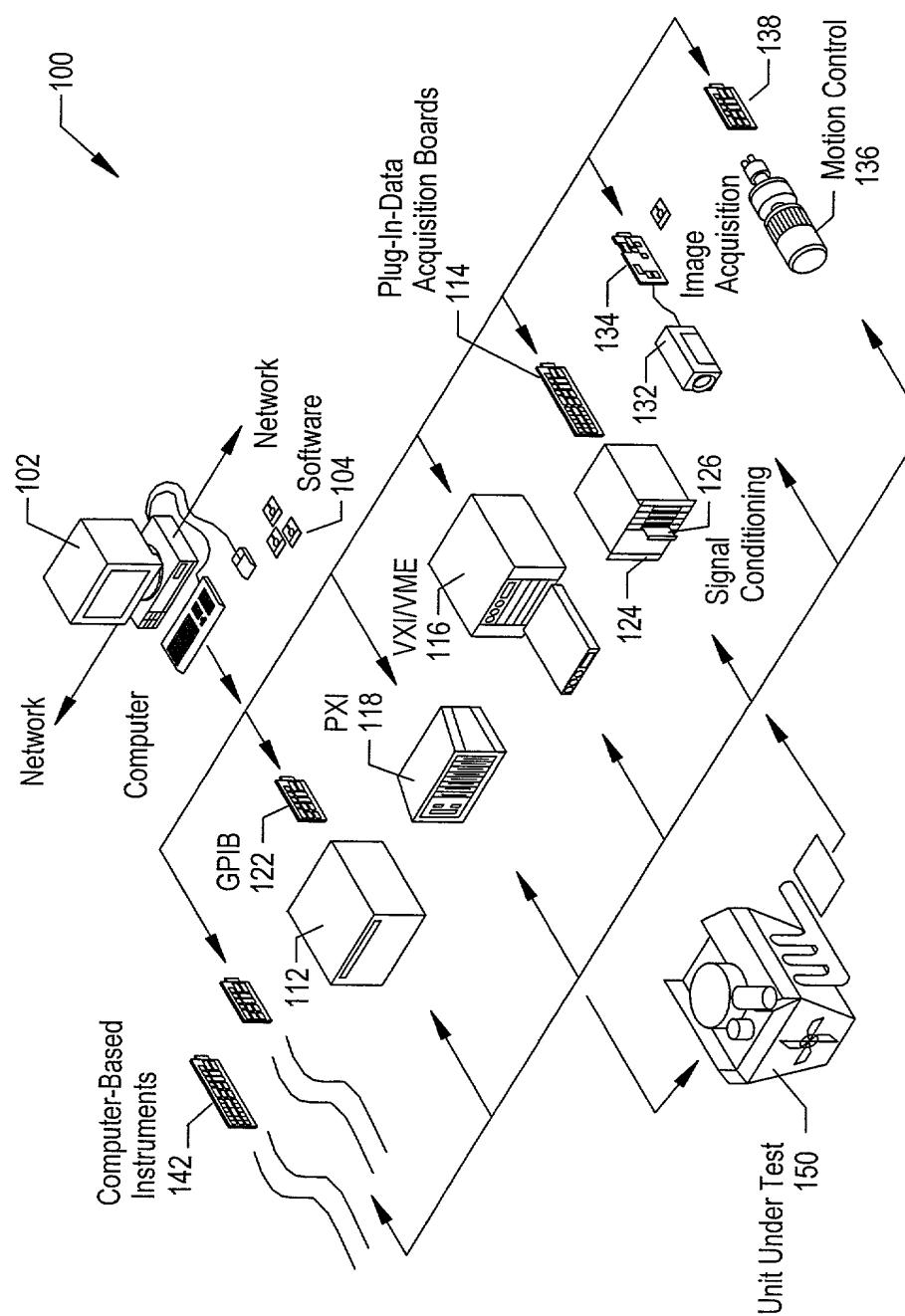


FIG. 1



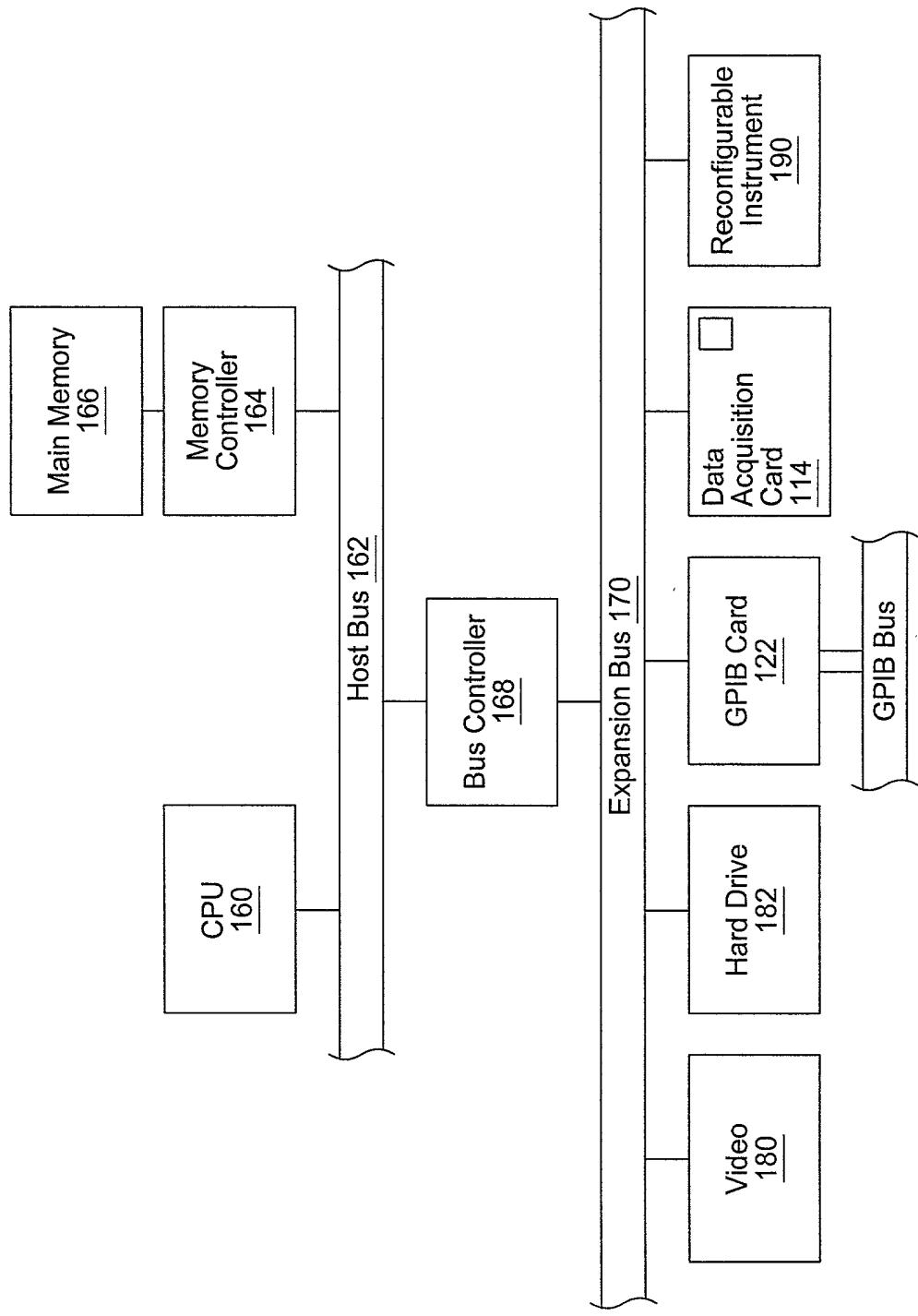


FIG. 2

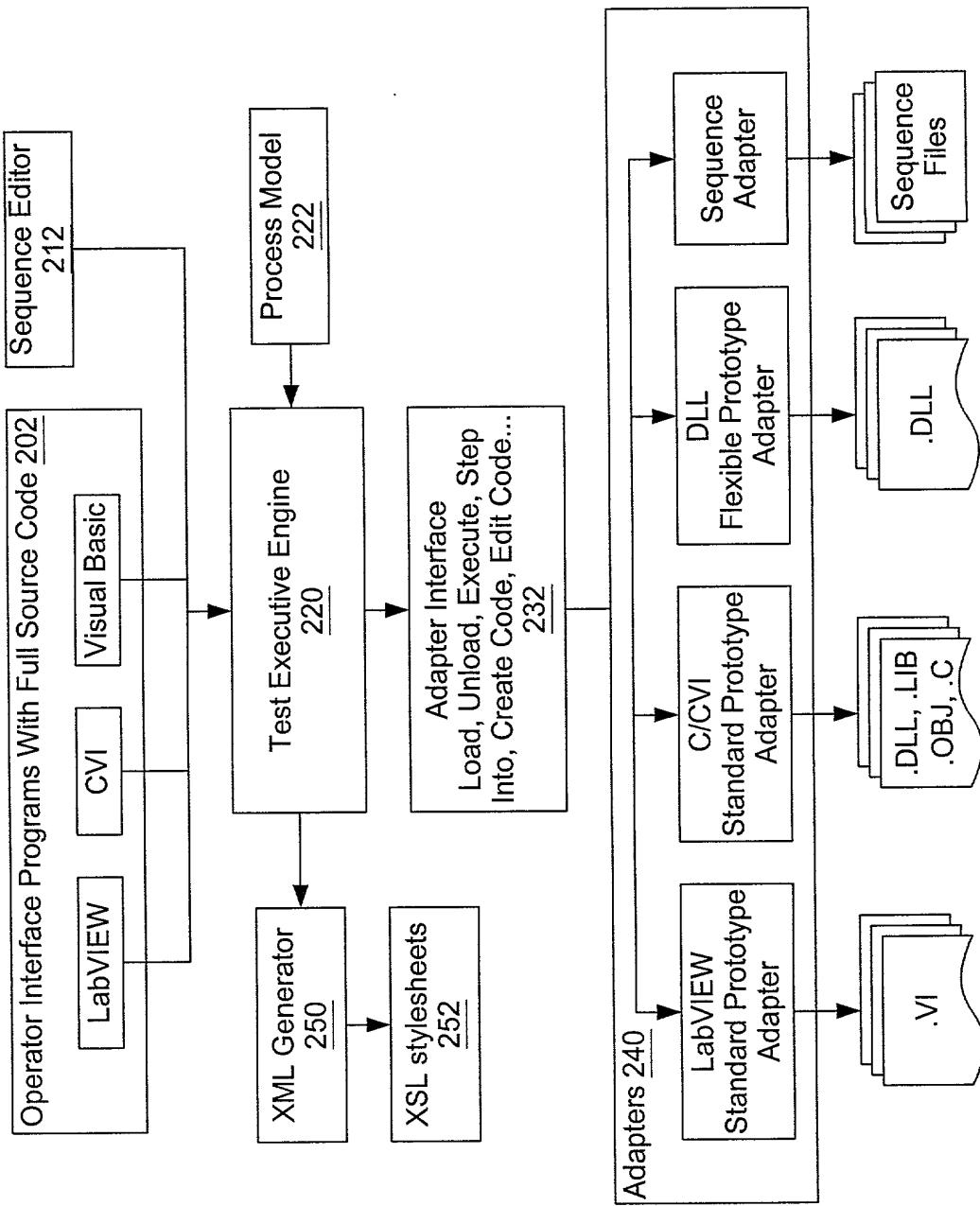


FIG. 3

C:\TestStand\Examples\XMLReports\computer.seq

Main | Setup | Cleanup | Parameters | Locals | View: MainSequence | Description | Execution Flow | Comment

Step

Power On

CPU

ROM

RAM

Video

Keyboard

ROM Diagnostics

RAM Diagnostics

Video Diagnostics

Keyboard Diagnostics

Numeric Limit Test, 12 <= x <= 15, volt, Call P...

Call MainSequence in cpu.seq

Pass/Fail Test, Call ROMTest (computer.dll)

Pass/Fail Test, Call RAMTest (computer.dll)

Numeric Limit Test, 70 <= x <= 75, hertz, Call ...

Pass/Fail Test, Call KeyboardTest (computer.dll)

Action, Call ROMDiagnostics (computer.dll)

Action, Call RAMDiagnostics (computer.dll)

Action, Call VideoDiagnostics (computer.dll)

Action, Call KeyboardDiagnostics (computer.dll)

Precondition

No Steps Selected | Number of Steps: 10

FIG. 4

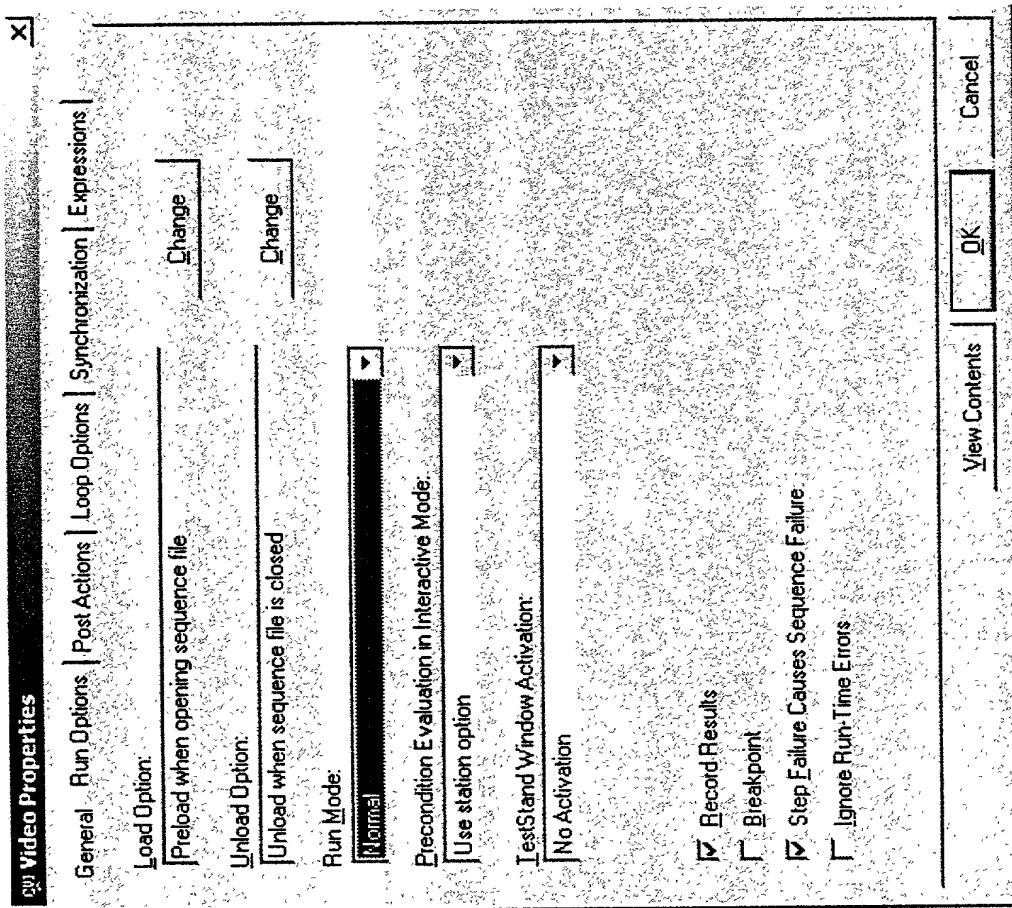


FIG. 5

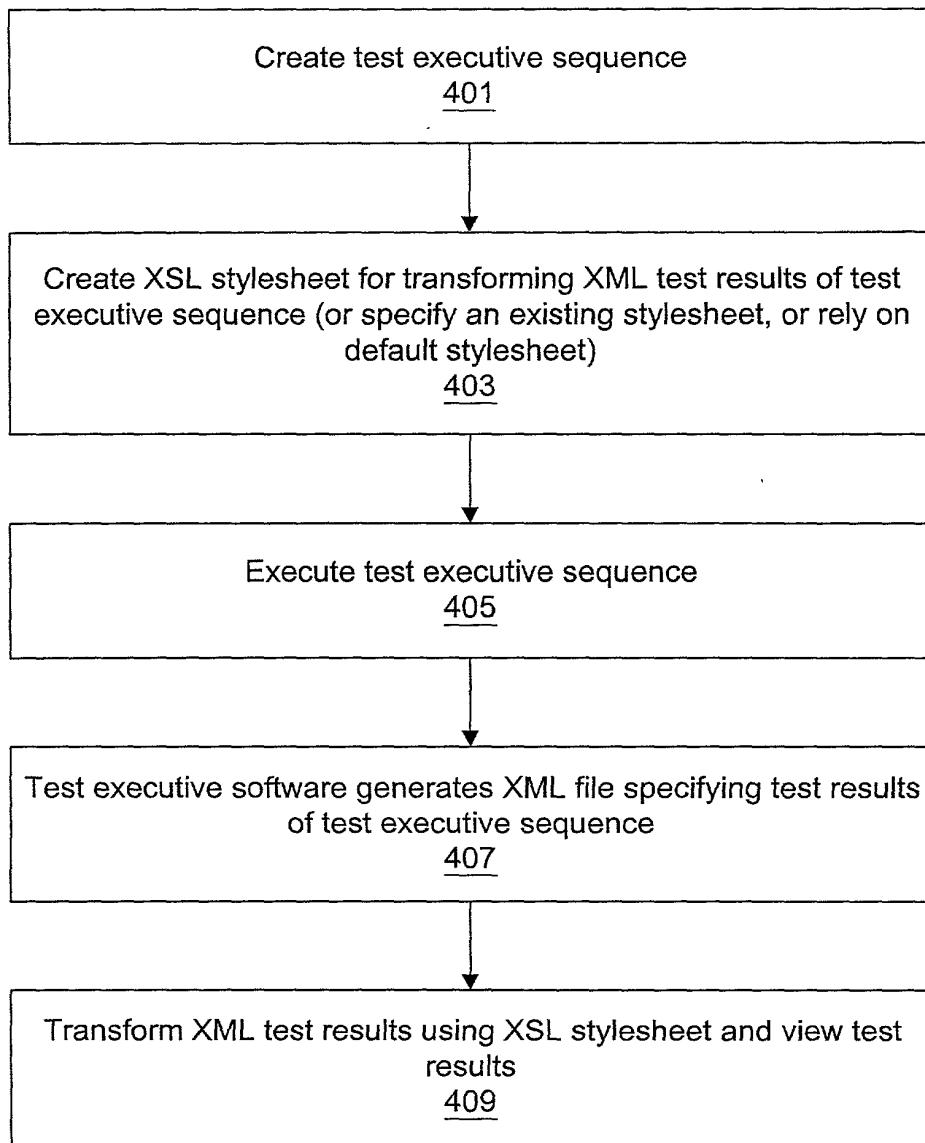


FIG. 6

## UUT Report

- **Station ID:** TCASTRO
- **Serial Number:** NONE
- **Socket Index:** 1
- **Date:** Friday, February 09, 2001
- **Time:** 3:21:59 PM
- **Operator:** administrator
- **Number of Results:** 18
- **UUT Result:** Failed
- **Failure Chain:**



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FIG. 7A

Begin Sequence: MainSequence  
(C:\TestStand\Examples\XML Reports\computer.seq)

Check 1: Run From Main Sequence	
Status:	Skipped
Explanation:	
Status:	Skipped
Pass test:	
Status:	Done
Power On:	
Status:	
Numeric:	15
Units:	volt
Limits:	
Low:	12
High:	18
Comp:	GELE (>= <=)
CH1	
Status:	

FIG. 7B

**Begin Sequence: MainSequence**  
(C:\TestStand\Examples\XMLReports\cpu.seq)

Processor	
Status:	True
Register	
Status:	True
PassFail:	True
Instruction Set	
Status:	True
PassFail:	True
Cache	
Status:	True
PassFail:	True
Memory	
Status:	True
PassFail:	True

**End Sequence: MainSequence**

<b>ROI</b>	
Status:	
PassFail:	True
<b>RAM</b>	
Status:	
PassFail:	False
<b>Video</b>	
Status:	
Numeric:	75
Units:	hertz
<b>Limits:</b>	
Low:	70
High:	75
Comp:	GELE ( $\geq$ $\leq$ )
<b>Keyboard</b>	
Status:	
PassFail:	True
<b>Power Diagnosis</b>	
Status:	Skipped
<b>RAM Diagnosis</b>	
Status:	None
<b>Video Diagnosis</b>	
Status:	Skipped
<b>Keyboard Diagnosis</b>	
Status:	Skipped

**End Sequence: MainSequence**

**End UUT Report**

**FIG. 7D**

## UUT Report

- Station ID: TCASTRO
- Serial Number: NONE
- Socket Index: 0
- Date: Friday, February 09, 2001
- Time: 3:21:59 PM
- Operator: administrator
- Number of Results: 18
- UUT Result: Passed

---

---

Begin Sequence: MainSequence  
(C:\TestStand\Examples\XMLReports\computer.seq)

Check If Run From Main Sequence	
Status:	Skipped
Explanation	
Status:	Skipped
Pre-test	
Status:	Done
Power On	
Status:	
Numeric:	15
Units:	volt
Limits:	
Low:	12
High:	18
Comp:	GE1E(==<=)
GPI	
Status:	

**Begin Sequence: MainSequence**  
(C:\TestStand\Examples\XMLReports\cpu.seq)

Part-Index	Value
Status:	None
<b>Register</b>	
Status:	None
PassFail:	True
<b>InstructionSet</b>	
Status:	None
PassFail:	True
<b>Cache</b>	
Status:	None
PassFail:	True
<b>GPU</b>	
Status:	None
PassFail:	True

**End Sequence: MainSequence**

<b>ROM</b>	
Status:	True
PassFail:	True
<b>RAM</b>	
Status:	True
PassFail:	True
<b>Net Freq</b>	
Status:	True
Numeric:	75
Units:	hertz
<b>Limits:</b>	
Low:	70
High:	75
Comp:	GELE(>=<=)
<b>Keyboard</b>	
Status:	True
PassFail:	True
<b>ROM Diagnostics</b>	
Status:	Skipped
<b>RAM Diagnostics</b>	
Status:	Skipped
<b>Video Diagnostics</b>	
Status:	Skipped
<b>Keyboard Diagnostics</b>	
Status:	Skipped

**End Sequence: MainSequence**

**End UUT Report**

**FIG. 7H**

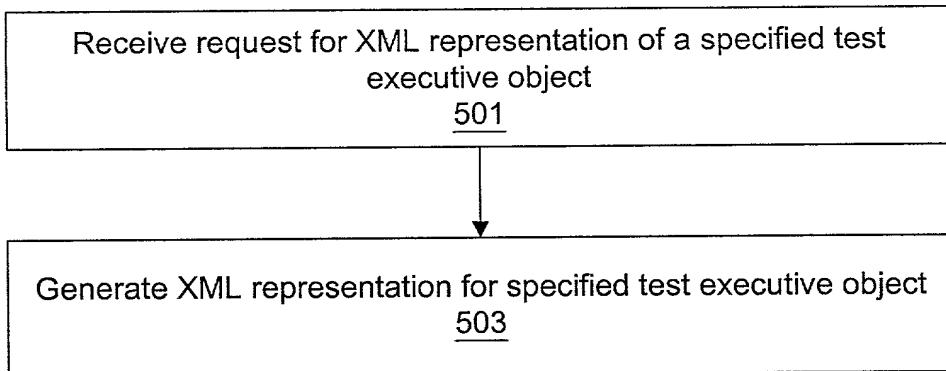


FIG. 8

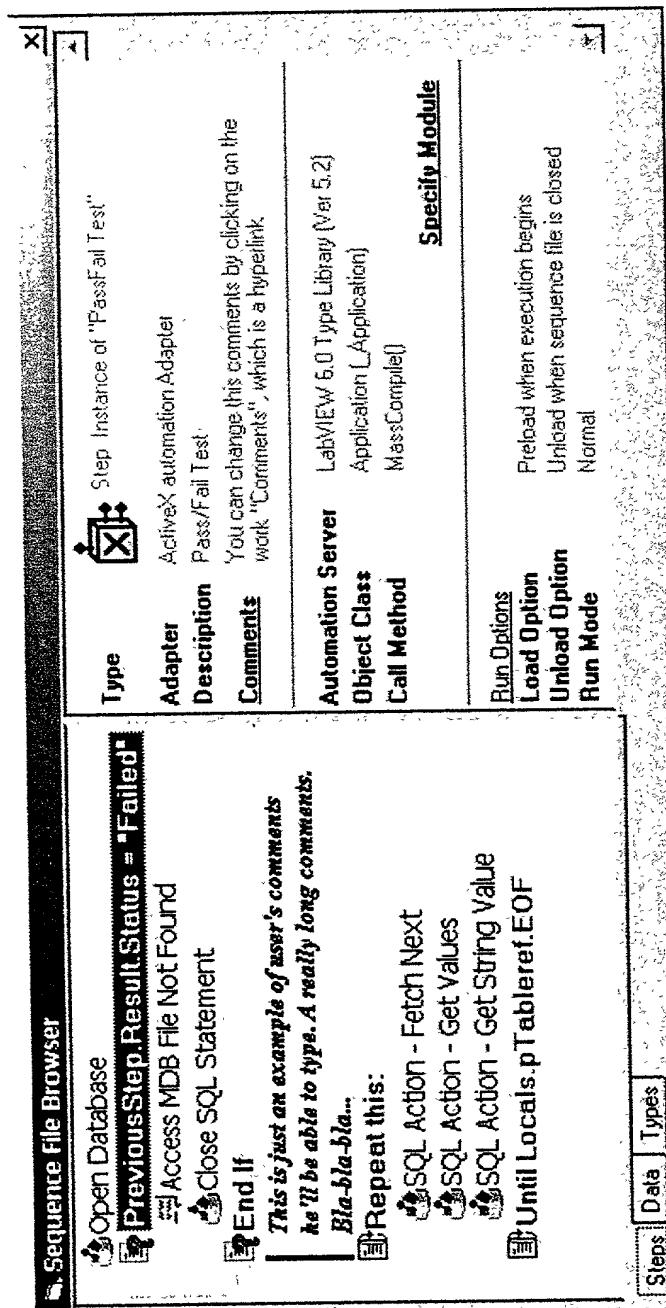


FIG. 9